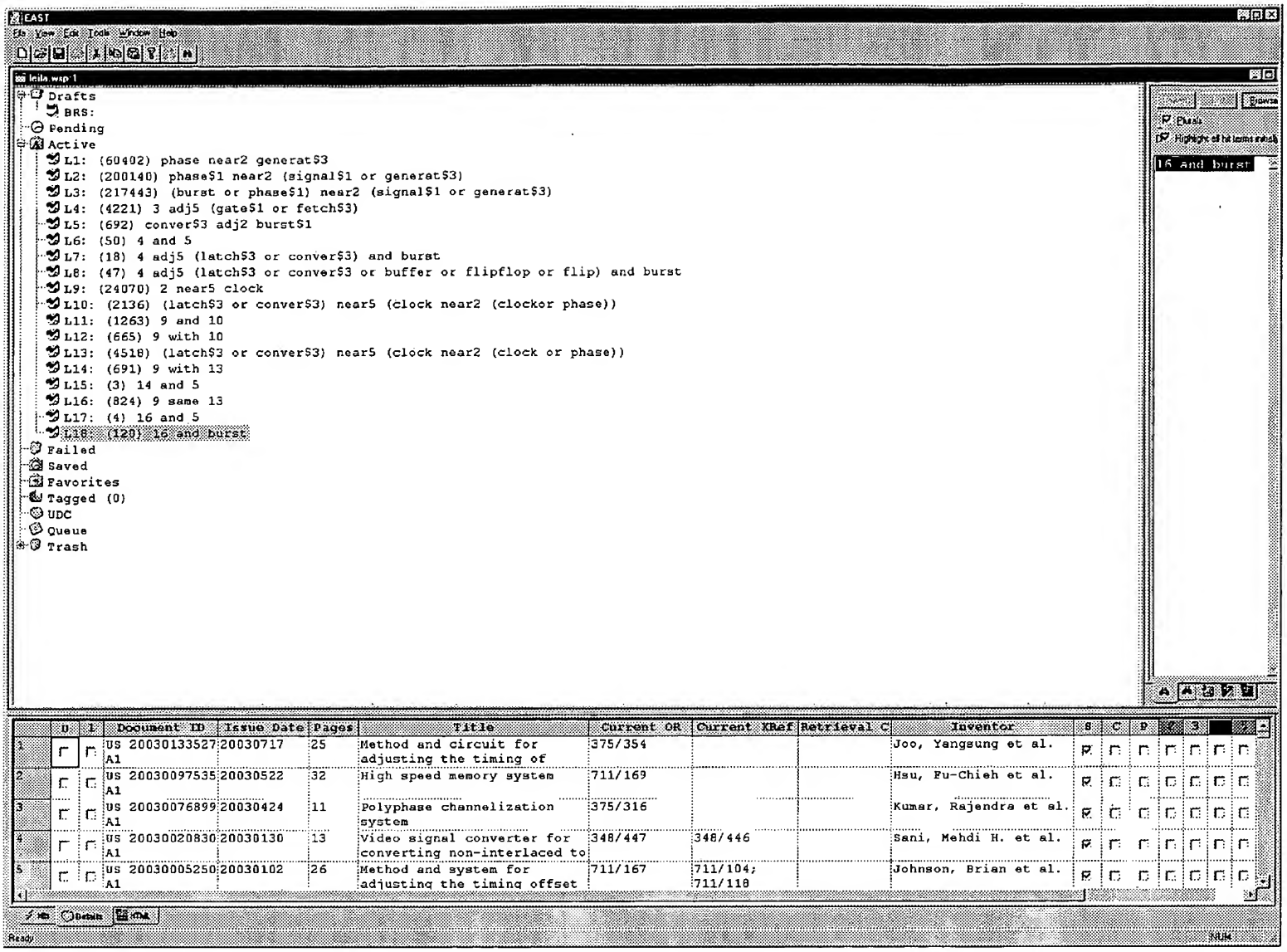
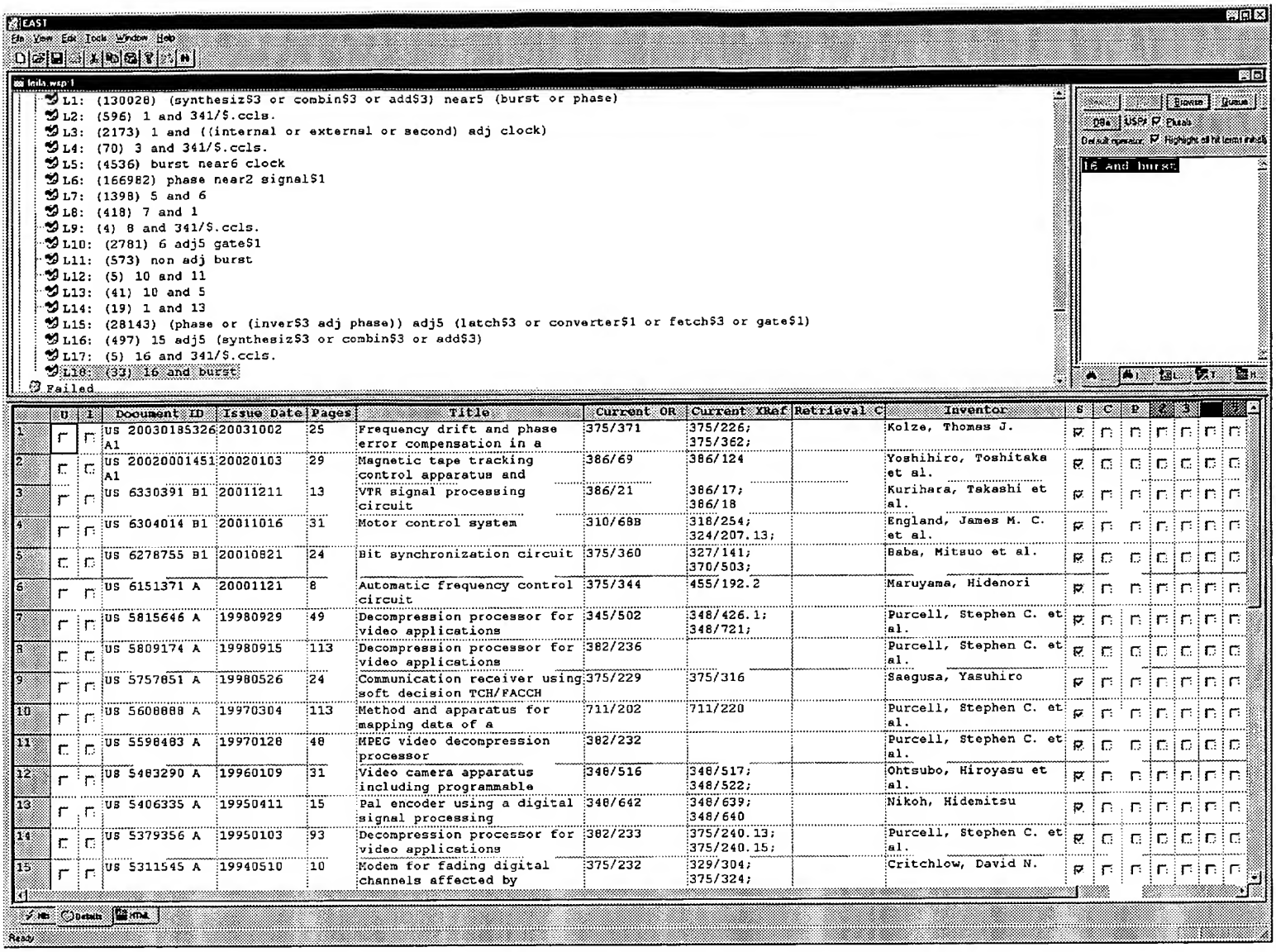


	D	L	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	D	2	3		7
1	☐	☐	US 20030133527 A1	20030717	25	Method and circuit for adjusting the timing of	375/354			Joo, Yangsung et al.	☑	☐	☐	☐	☐	☐	☐
2	☐	☐	US 20030097535 A1	20030522	32	High speed memory system	711/169			Hsu, Fu-Chieh et al.	☑	☐	☐	☐	☐	☐	☐
3	☐	☐	US 20030076899 A1	20030424	11	Polyphase channelization system	375/316			Kumar, Rajendra et al.	☑	☐	☐	☐	☐	☐	☐
4	☐	☐	US 20030020830 A1	20030130	13	Video signal converter for converting non-interlaced to	348/447	348/446		Sani, Mehdi H. et al.	☑	☐	☐	☐	☐	☐	☐
5	☐	☐	US 20030005250 A1	20030102	26	Method and system for adjusting the timing offset	711/167	711/104; 711/118		Johnson, Brian et al.	☑	☐	☐	☐	☐	☐	☐



	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	2	3
1			US 20030185326 A1	20031002	25	Frequency drift and phase error compensation in a	375/371	375/226; 375/362;		Kolze, Thomas J.					
2			US 20020001451 A1	20020103	29	Magnetic tape tracking control apparatus and	386/69	386/124		Yoshihiro, Toshitaka et al.					
3			US 6330391 B1	20011211	13	VTR signal processing circuit	386/21	386/17; 386/18		Kurihara, Takashi et al.					
4			US 6304014 B1	20011016	31	Motor control system	310/68B	318/254; 324/207.13;		England, James M. C. et al.					
5			US 6278755 B1	20010821	24	Bit synchronization circuit	375/360	327/141; 370/503;		Baba, Mitsuo et al.					
6			US 6151371 A	20001121	8	Automatic frequency control circuit	375/344	455/192.2		Maruyama, Hidenori					
7			US 5815646 A	19980929	49	Decompression processor for video applications	345/502	348/426.1; 348/721;		Purcell, Stephen C. et al.					
8			US 5809174 A	19980915	113	Decompression processor for video applications	382/236			Purcell, Stephen C. et al.					
9			US 5757851 A	19980526	24	Communication receiver using soft decision TCH/FACCH	375/229	375/316		Saegusa, Yasuhiro					
10			US 5608888 A	19970304	113	Method and apparatus for mapping data of a	711/202	711/220		Purcell, Stephen C. et al.					
11			US 5598483 A	19970128	48	MPEG video decompression processor	382/232			Purcell, Stephen C. et al.					
12			US 5483290 A	19960109	31	Video camera apparatus including programmable	348/516	348/517; 348/522;		Ohtsubo, Hiroyasu et al.					
13			US 5406335 A	19950411	15	Pal encoder using a digital signal processing	348/642	348/639; 348/640		Nikoh, Hidemitsu					
14			US 5379356 A	19950103	93	Decompression processor for video applications	382/233	375/240.13; 375/240.15;		Purcell, Stephen C. et al.					
15			US 5311545 A	19940510	10	Modem for fading digital channels affected by	375/232	329/304; 375/324;		Critchlow, David N.					